

Abstract

5 A bus compression apparatus for compressing data is
provided to suppress an EMI signal and to simplify a data
bus structure. In the apparatus, the voltage levels of
the digital output signals are summed in accordance with
the resistance values of the data compression circuit to
produce a compressed analog signal. The compressed analog
signal is transmitted through a bus lines to a data
decompressor which reproduces the digital data in response
10 to the voltage levels of the compressed analog signal.

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